



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	System And Method Of Controlling Software Decompression Through Exceptions
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Application Number: 09/925314

Confirmation Number: 4782

First Named Applicant: Christopher RISUCCI

Attorney Docket Number: 1778.0180000

Art Unit: 2183

Examiner: Henry Tsai

Search string: (5031096 or 5241679 or 5517664 or 6212630 or 4068303 or 4463342 or 4488143 or 4603399 or 4799242 or 4802119 or 4814975 or 5115500 or 5463700 or 5467134 or 5568646 or 5577200 or 5619666 or 5632024 or 5745058 or 5758115 or 5764994 or 5794010 or 5829012 or 5854913 or 5896519 or 5954830 or 6021265 or 6219774 or 20020138715).pn.



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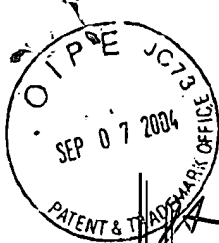
SEP 10 2004

Technology Center 2100

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	5031096	1991-07-09	Jen et al.			
2	2	5241679	1993-08-31	Nakagawa et al.			
3	3	5517664	1996-05-14	Watanabe et al.			
4	4	6212630	2001-04-03	Takayama et al.	B1		
5	5	4068303	1978-01-10	Morita			
6	6	4463342	1984-07-31	Langdon, Jr. et al.			
7	7	4488143	1984-12-11	Martin			
8	8	4603399	1986-07-29	Cheek et al.			
9	9	4799242	1989-01-17	Vermeulen			
10	10	4802119	1989-01-31	Heene et al.			
11	11	4814975	1989-03-21	Hirosawa et al.			
12	12	5115500	1992-05-19	Larsen			



13	5463700	1995-10-31	Nakazawa
14	5467134	1995-11-14	Laney et al.
15	5568646	1996-10-22	Jaggar
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17	5619666	1997-04-08	Coon et al.
18	5632024	1997-05-20	Yajima et al.
19	5745058	1998-04-28	Auerbach et al.
20	5758115	1998-05-26	Nevill
21	5764994	1998-06-09	Craft
22	5794010	1998-08-11	Worrell et al.
23	5829012	1998-10-27	Marlan et al.
24	5854913	1998-12-29	Goetz et al.
25	5896519	1999-04-20	Worrell
26	5954830	1999-09-21	Ternullo, Jr.
27	6021265	2000-02-01	Nevill
28	6219774	2001-04-17	Hammond et al.

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B1

US Published Applications

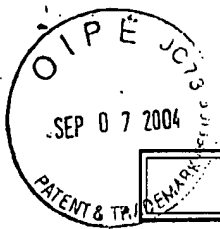
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init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
45	1	20020138715	2002-09-26	Minematsu	A1		

Signature


Examiner Name	Date
<i>Henry Jan</i>	11/14/04

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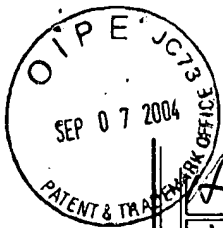
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US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
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15	1	3543245	1970-11-24	Nutter			
15	2	3631405	1971-12-28	Hoff et al.			
15	3	3949372	1976-04-06	Brioschi			
15	4	4077058	1978-02-28	Appell et al.			
15	5	4084235	1978-04-11	Hirtle et al.			
15	6	4274138	1981-06-16	Shimokawa			
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15	9	4575797	1986-03-11	Gruner et al.			



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11	4774652	1988-09-27	Dhuey et al.
12	4782443	1988-11-01	Matsumoto
13	4835734	1989-05-30	Kodaira et al.
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16	4876639	1989-10-24	Mensch, Jr.
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18	5091846	1992-02-25	Sachs et al.
19	5193158	1993-03-09	Kinney et al.
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21	5280593	1994-01-18	Bullions, III et al.
22	5307504	1994-04-26	Robinson et al.
23	5335331	1994-08-02	Murao et al.
24	5339422	1994-08-16	Brender et al.
25	5420992	1995-05-30	Killian et al.
26	5430862	1995-07-04	Smith et al.
27	5438668	1995-08-01	Coon et al.
28	5438672	1995-08-01	Dey
29	5481684	1996-01-02	Richter et al.
30	5481693	1996-01-02	Blomgren et al.
31	5519873	1996-05-21	Butter et al.
32	5542059	1996-07-30	Blomgren
33	5574887	1996-11-12	Fitch
34	5574927	1996-11-12	Scantlin
35	5574928	1996-11-12	White et al.
36	5598546	1997-01-28	Blomgren
37	5619665	1997-04-08	Emma
38	5638525	1997-06-10	Hammond et al.
39	5652852	1997-07-29	Yokota
40	5685009	1997-11-04	Blomgren et al.
41	5740461	1998-04-14	Jaggar
42	5774686	1998-06-30	Hammond et al.
43	5781750	1998-07-14	Blomgren et al.
44	5796973	1998-08-18	Witt et al.
45	5828859	1998-10-27	Tanihira et al.

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<input checked="" type="checkbox"/>	46	5867682	1999-02-01	Witt et al.	
<input type="checkbox"/>	47	5905893	1999-05-18	Worrell	
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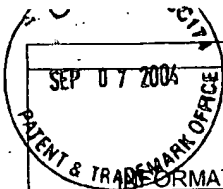
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Examiner Name	Date
<i>Harry Fran</i>	11/14/04

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FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.

1778.0180000

APPLICATION NO :

09/925,314

FIRST NAMED INVENTOR

Risucci, Christopher R.

FILING DATE

August 10, 2001

ART UNIT

2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1					
	AB1					
	AC1					
	AD1					
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	AI1					
	AJ1					
	AK1					

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
HA	AL1	0 109 567 A2	05/1984	EP		Yes No
	AM1	0 169 565 A2	01/1986	EP		Yes No
	AN1	0 199 173 A2 & A3	10/1986	EP		Yes No
	AO1	0 264 215 A2	04/1988	EP		Yes No
HA	AP1	0 272 198 A2 & A3	06/1988	EP		Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

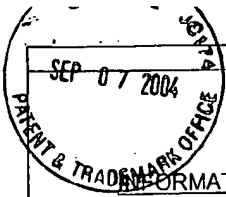
HA	AR	1	Bandyopadhyay, A. and Zheng, Y.F., "Combining Both Micro-Code And Hardwired Control In RISC," 5 pages, (published in ACM SIGARCH Computer Architecture News, Vol. 15, Issue 4, pp. 11-15 (September 1987)).
HA	AS	1	Bursky, D., "Software-Efficient RISC Core Trims System-Memory Needs," Reprinted from Electronic Design, Penton Publishing, Inc., 3 pages (March 20, 1995).
HA	AT	1	Hayashi, T. et al., "A 5.6-MIPS Call-Handling Processor for Switching Systems," IEEE Journal of Solid-State Circuits, Vol. 24, No. 4, IEEE, pp. 945-950 (August 1989).

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DATE CONSIDERED

11/14/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



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ATTY. DOCKET NO.
1778.0180000APPLICATION NO
09/925,314FIRST NAMED INVENTOR
Risucci, Christopher R.FILING DATE
August 10, 2001ART UNIT
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1A	AL2	0 324 308 A2 & A3	07/1989	EP			Yes No
	AM2	2 016 755 A	09/1979	GB			Yes No
	AN2	2 284 492 A	06/1995	GB			Yes No
	AO2	2 289 353 A	11/1995	GB			Yes No
2A	AP2	2 290 395 A	12/1995	GB			Yes No

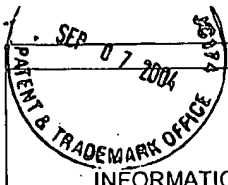
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

1A	AR	2	"High Performance Dual Architecture Processor," IBM Technical Disclosure Bulletin, Vol. 36, No. 2, IBM Corp., pp. 231-234 (February 1993).
1A	AS	2	Intel486TM Microprocessor Family Programmer's Reference Manual, Intel Corporation, 1992, pp. 23-5, 23-6, and 23-9.
1A	AT	2	McNeley, K.J. and Milutinovic, V.M., "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," IEEE Micro, IEEE, pp. 60-72 (February 1987).

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129	AL3	96/24895	08/1996	WO			Yes No
1	AM3	95/30187	11/1995	WO			Yes No
1	AN3	95/30188	11/1995	WO			Yes No
2	AO3	990394	06/1976	CA			Yes No
	AP3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

129	AR	3	"LSI Tiny Risc development," http://www.redhat.com/support/manuals/gnupro99r1/6_embed/emb09.html , 13 pages (April 2001).
129	AS	3	V _R 4121™ 64/32-Bit Microprocessor μPD30121, NEC Corporation 1998, Document No. U13569EJ4V1UM00 (4 th edition), pages 1-19 and 103-131, Published July 2000.
12	AT	3	

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